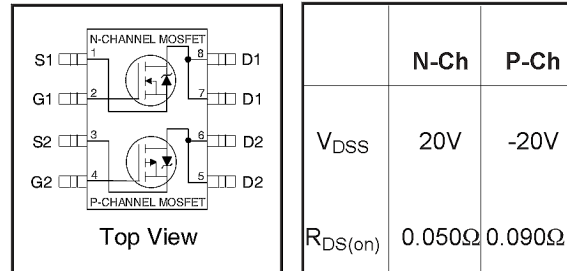


IRF7307QPbF

HEXFET® Power MOSFET

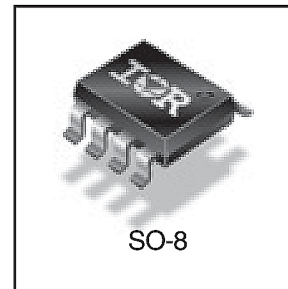
- Advanced Process Technology
- Ultra Low On-Resistance
- Dual N and P Channel MOSFET
- Surface Mount
- Available in Tape & Reel
- 150°C Operating Temperature
- Automotive [Q101] Qualified
- Lead-Free



Description

Specifically designed for Automotive applications, these HEXFET® Power MOSFET's in a Dual SO-8 package utilize the latest processing techniques to achieve extremely low on-resistance per silicon area. Additional features of these Automotive qualified HEXFET Power MOSFET's are a 150°C junction operating temperature, fast switching speed and improved repetitive avalanche rating. These benefits combine to make this design an extremely efficient and reliable device for use in Automotive applications and a wide variety of other applications.

The efficient SO-8 package provides enhanced thermal characteristics and dual MOSFET die capability making it ideal in a variety of power applications. This dual, surface mount SO-8 can dramatically reduce board space and is also available in Tape & Reel.



Absolute Maximum Ratings

	Parameter	Max.		Units
		N-Channel	P-Channel	
$I_D @ T_A = 25^\circ\text{C}$	10 Sec. Pulse Drain Current, $V_{GS} @ 4.5\text{V}$	5.7	-4.7	A
$I_D @ T_A = 25^\circ\text{C}$	Continuous Drain Current, $V_{GS} @ 4.5\text{V}$	5.2	-4.3	
$I_D @ T_A = 70^\circ\text{C}$	Continuous Drain Current, $V_{GS} @ 4.5\text{V}$	4.1	-3.4	
I_{DM}	Pulsed Drain Current ①	21	-17	
$P_D @ T_A = 25^\circ\text{C}$	Power Dissipation	2.0		W
	Linear Derating Factor	0.016		W/°C
V_{GS}	Gate-to-Source Voltage	± 12		V
dv/dt	Peak Diode Recovery dv/dt ②	5.0	-5.0	V/ns
T_J, T_{STG}	Junction and Storage Temperature Range	-55 to + 150		°C

Thermal Resistance Ratings

	Parameter	Typ.	Max.	Units
$R_{\theta JA}$	Maximum Junction-to-Ambient③	---	62.5	°C/W

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Electrical Characteristics @ T_J = 25°C (unless otherwise specified)

Parameter	Description		Min.	Typ.	Max.	Units	Conditions
V _{(BR)DSS}	Drain-to-Source Breakdown Voltage	N-Ch	20	—	—	V	V _{GS} = 0V, I _D = 250μA
		P-Ch	-20	—	—		V _{GS} = 0V, I _D = -250μA
ΔV _{(BR)DSS} /ΔT _J	Breakdown Voltage Temp. Coefficient	N-Ch	—	0.044	—	V/°C	Reference to 25°C, I _D = 1mA
		P-Ch	—	-0.012	—		Reference to 25°C, I _D = -1mA
R _{DS(ON)}	Static Drain-to-Source On-Resistance	N-Ch	—	—	0.050	Ω	V _{GS} = 4.5V, I _D = 2.6A ③
		N-Ch	—	—	0.070		V _{GS} = 2.7V, I _D = 2.2A ③
		P-Ch	—	—	0.090		V _{GS} = -4.5V, I _D = -2.2A ③
		P-Ch	—	—	0.140		V _{GS} = -2.7V, I _D = -1.8A ③
V _{GS(th)}	Gate Threshold Voltage	N-Ch	0.70	—	—	V	V _{DS} = V _{GS} , I _D = 250μA
		P-Ch	-0.70	—	—		V _{DS} = V _{GS} , I _D = -250μA
g _{fs}	Forward Transconductance	N-Ch	8.30	—	—	S	V _{DS} = 15V, I _D = 2.6A ③
		P-Ch	4.00	—	—		V _{DS} = -15V, I _D = -2.2A ③
I _{DSS}	Drain-to-Source Leakage Current	N-Ch	—	—	1.0	μA	V _{DS} = 16V, V _{GS} = 0V
		P-Ch	—	—	-1.0		V _{DS} = -16V, V _{GS} = 0V
		N-Ch	—	—	25		V _{DS} = 16V, V _{GS} = 0V, T _J = 125°C
		P-Ch	—	—	-25		V _{DS} = -16V, V _{GS} = 0V, T _J = 125°C
I _{GSS}	Gate-to-Source Forward Leakage	N-P	—	—	±100		V _{GS} = ±12V
Q _g	Total Gate Charge	N-Ch	—	—	20	nC	N-Channel I _D = 2.6A, V _{DS} = 16V, V _{GS} = 4.5V ③
		P-Ch	—	—	22		
Q _{gs}	Gate-to-Source Charge	N-Ch	—	—	2.2	nC	P-Channel I _D = -2.2A, V _{DS} = -16V, V _{GS} = -4.5V ③
		P-Ch	—	—	3.3		
Q _{gd}	Gate-to-Drain ("Miller") Charge	N-Ch	—	—	8.0	nC	
		P-Ch	—	—	9.0		
t _{d(on)}	Turn-On Delay Time	N-Ch	—	9.0	—	ns	N-Channel V _{DD} = 10V, I _D = 2.6A, R _G = 6.0Ω, R _D = 3.8Ω ③
		P-Ch	—	8.4	—		
t _r	Rise Time	N-Ch	—	42	—	ns	P-Channel V _{DD} = -10V, I _D = -2.2A, R _G = 6.0Ω, R _D = 4.5Ω ③
		P-Ch	—	26	—		
t _{d(off)}	Turn-Off Delay Time	N-Ch	—	32	—	ns	
		P-Ch	—	51	—		
t _f	Fall Time	N-Ch	—	51	—	ns	
		P-Ch	—	33	—		
L _D	Internal Drain Inductance	N-P	—	4.0	—	nH	Between lead tip and center of die contact
L _S	Internal Source Inductance	N-P	—	6.0	—		
C _{iss}	Input Capacitance	N-Ch	—	660	—	pF	N-Channel V _{GS} = 0V, V _{DS} = 15V, f = 1.0MHz ③
		P-Ch	—	610	—		
C _{oss}	Output Capacitance	N-Ch	—	280	—	pF	P-Channel V _{GS} = 0V, V _{DS} = -15V, f = 1.0MHz ③
		P-Ch	—	310	—		
C _{riss}	Reverse Transfer Capacitance	N-Ch	—	140	—		
		P-Ch	—	170	—		

Source-Drain Ratings and Characteristics

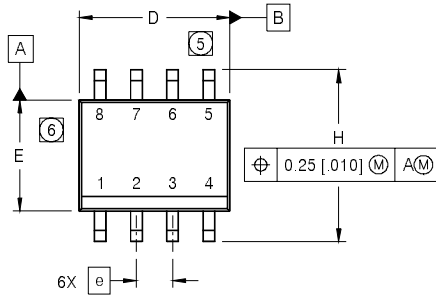
Parameter	Description		Min.	Typ.	Max.	Units	Conditions	
I _S	Continuous Source Current (Body Diode)	N-Ch	—	—	2.5	A		
		P-Ch	—	—	-2.5			
I _{SM}	Pulsed Source Current (Body Diode) ①	N-Ch	—	—	21	A		
		P-Ch	—	—	-17			
V _{SD}	Diode Forward Voltage	N-Ch	—	—	1.0	V	T _J = 25°C, I _S = 1.8A, V _{GS} = 0V ③	
		P-Ch	—	—	-1.0		T _J = 25°C, I _S = -1.8A, V _{GS} = 0V ③	
t _{rr}	Reverse Recovery Time	N-Ch	—	29	44	ns	N-Channel T _J = 25°C, I _F = 2.6A, di/dt = 100A/μs ③	
		P-Ch	—	56	84			
Q _{rr}	Reverse Recovery Charge	N-Ch	—	22	33	nC	P-Channel T _J = 25°C, I _F = -2.2A, di/dt = 100A/μs ③	
		P-Ch	—	71	110			
t _{on}	Forward Turn-On Time	N-P	Intrinsic turn-on time is negligible (turn-on is dominated by L _S +L _D)					

Notes:

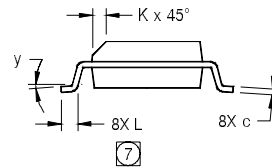
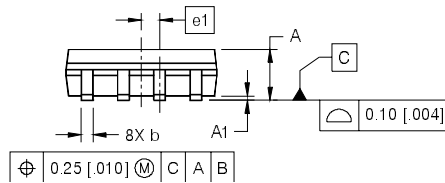
- ① Repetitive rating; pulse width limited by max. junction temperature. (See fig. 23)
- ② N-Channel I_{SD} ≤ 2.6A, di/dt ≤ 100A/μs, V_{DD} ≤ V_{(BR)DSS}, T_J ≤ 150°C
P-Channel I_{SD} ≤ -2.2A, di/dt ≤ 50A/μs, V_{DD} ≤ V_{(BR)DSS}, T_J ≤ 150°C
- ③ Pulse width ≤ 300μs; duty cycle ≤ 2%.
- ④ Surface mounted on FR-4 board, t ≤ 10sec.

SO-8 Package Outline

Dimensions are shown in millimeters (inches)



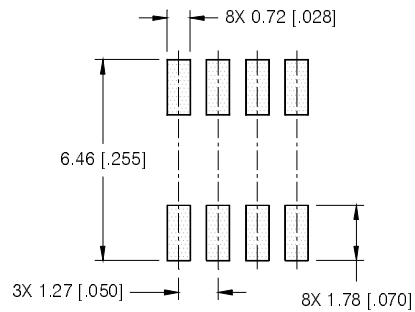
DIM	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A	.0532	.0688	1.35	1.75
A1	.0040	.0098	0.10	0.25
b	.013	.020	0.33	0.51
c	.0075	.0098	0.19	0.25
D	.189	.1968	4.80	5.00
E	.1497	.1574	3.80	4.00
e	.050 BASIC		1.27 BASIC	
e 1	.025 BASIC		0.635 BASIC	
H	.2284	.2440	5.80	6.20
K	.0099	.0196	0.25	0.50
L	.016	.050	0.40	1.27
y	0°	8°	0°	8°



NOTES:

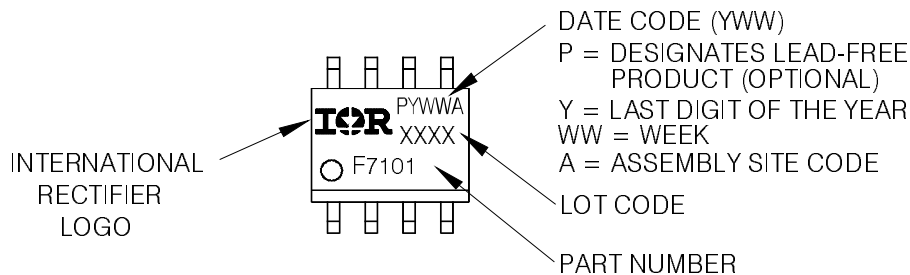
- DIMENSIONING & TOLERANCING PER ASME Y14.5M-1994.
- CONTROLLING DIMENSION: MILLIMETER
- DIMENSIONS ARE SHOWN IN MILLIMETERS [INCHES].
- OUTLINE CONFORMS TO JEDEC OUTLINE MS-012AA.
- 5** DIMENSION DOES NOT INCLUDE MOLD PROTRUSIONS. MOLD PROTRUSIONS NOT TO EXCEED 0.15 [0.006].
- 6** DIMENSION DOES NOT INCLUDE MOLD PROTRUSIONS. MOLD PROTRUSIONS NOT TO EXCEED 0.25 [0.010].
- 7** DIMENSION IS THE LENGTH OF LEAD FOR SOLDERING TO A SUBSTRATE.

FOOTPRINT



SO-8 Part Marking

EXAMPLE: THIS IS AN IRF7101 (MOSFET)

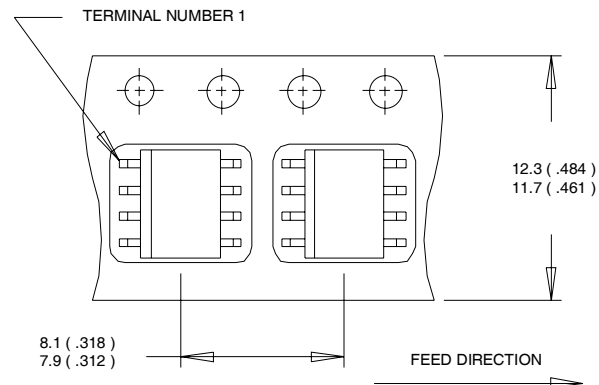


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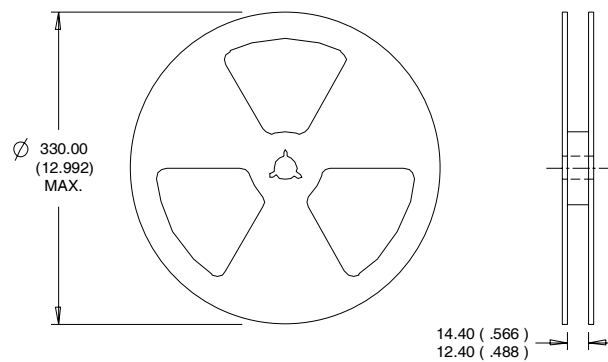
SO-8 Tape and Reel

Dimensions are shown in millimeters (inches)



NOTES:

1. CONTROLLING DIMENSION : MILLIMETER.
2. ALL DIMENSIONS ARE SHOWN IN MILLIMETERS(INCHES).
3. OUTLINE CONFORMS TO EIA-481 & EIA-541.



NOTES :

1. CONTROLLING DIMENSION : MILLIMETER.
2. OUTLINE CONFORMS TO EIA-481 & EIA-541.

Data and specifications subject to change without notice.
This product has been designed and qualified for the Automotive [Q101] market.
Qualification Standards can be found on IR's Web site.

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